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Docket No.: J&R-0694

CERTIFICATION OF MAILING OR TRANSMISSION

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F. Donald Paris

June 6, 2005
Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/922,479 Confirmation No.: 7212
Inventor : Thomas Zettler
Filed : August 3, 2001
Title : Method and Device for Testing an
Integrated Circuit, Integrated Circuit to
be Tested, and Wafer with a Large Number
of Integrated Circuits to be Tested
TC/A.U. : 2133
Examiner : Phung M. Chung
Customer No. : 24131

A M E N D M E N T

Mail Stop No Fee Amendment
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the Office action dated March 31, 2005, kindly
amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of
claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 13 of this paper.